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PATENTS LT-146

### THE UNITED STATES PATENT AND TRADEMARK OFFICE

### PATENT APPLICATION

Applicant

Steven D. Roach

Application No. :

10/015,972

Confirmation No.: 6870

Filed

November 1, 2001

For

CIRCUITS AND METHODS FOR CURRENT

MEASUREMENTS REFERRED TO A PRECISION

**IMPEDANCE** 

Group Art Unit

2819

Examiner

Not yet assigned

Hon. Commissioner

for Patents

June 25, 2002

Washington, D.C. 20231

New York, New York

## SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Sir:

In accordance with 37 C.F.R. §§ 1.56 and 1.97, applicants wish to call the attention of the Examiner to the following document:

# **U.S.** Patents

Evel

3,573,644

April 6, 1971

Roach 5,121,075

June 9, 1992

### Other Documents

Addis, John, "Three technologies on one chip make a broadband amplifier", <u>Electronics The International</u>

<u>Magazine of Electronics Technology</u>, June 5, 1972, pp. 103107.

Ahuja, B.K., "Implementation of Active Distributed RC Anti-Aliasing/Smoothing Filters", <u>IEEE</u> <u>Journal of Solid-State Circuits</u>, Vol. SC-17, No. 6, pp. 339-342.

Dascher, David J., "Measuring Parasitic Capacitance and Inductance Using TDR", <u>Hewlett-Packard Journal</u>, April 1996, pp. 1-19.

Ikalainen, Pertti, K. "An RLC Matching Network and Application in 1-20 GHZ Monolithic Amplifier", <u>IEEE MTT-S International Microwave Symposium Digest</u>, Vol. I, 1989, pp. 1115-1118.

Khoury, John, M., "Synthesis of Arbitrary Rational Transfer Funtions is S Using Uniform Distributed RC Active Circuits", <u>IEEE Transactions on Circuits and Systems</u>, Vol. 37, No. 4, April 1990, pp. 464-472.

Khoury, John, M., "On the Design of Constant Settling Time AGC Circuits", <u>IEEE Transactions on Circuits and Systems</u>, Vol. 45, No. 3, March 1998, pp. 283-294.

MAXIM, "Interfacing Maxim Laser Drivers with Laser Diodes", May 2000, pp. 1-12.

Sackinger, Eduard, et al., "A 3GHz, 32dB CMOS Limiting Amplifier for SONET OC-48 Receivers", <u>IEEE International Solid-State Circuits Conference</u>, 2000, pg. 158.

Swartz, R.G. et al., "An Integrated Circuit for Multiplexing and Driving Injection Lasers", <u>IEEE Journal of Solid-State Circuits</u>, Vol. SC-17, No. 4, August 1982, pp. 753-760.

A copy of this document is enclosed herewith. It is respectfully requested that the document be: (1) fully considered by the Patent and Trademark Office during the examination of this application; and (2) printed on any patent which may issue on this application. Applicant requests that a copy of Form PTO-1449 (submitted in

duplicate herewith), as considered and initialed by the Examiner, be returned with the next communication.

Applicant believes that no fee is due at this However, the Director is hereby authorized to charge any fee, or credit any overpayment, that may be due in connection with this Information Disclosure Statement, to Deposit Account No. 06-1075. A duplicate copy of this Information Disclosure Statement is enclosed herewith.

An early and favorable action is respectfully requested.

Respectfully submitted,

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope

addressed to:

Commissioner for Patents, P.O. Box 2327 ARIGNAtion, V.A. 20282, on

June 25,2002

William Baiky Name of person signing

Signature of person signifig

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